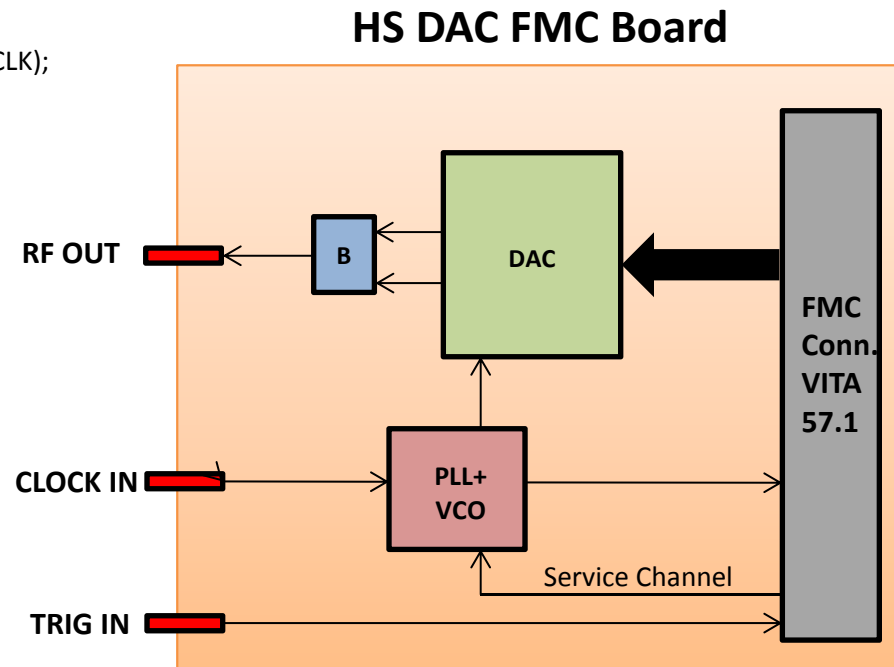


HS DAC FMC Board: Overview

- **RF OUTPUT CHARACTERISTICS:**
 - Output Mode: Single-ended (A.C. Coupled);
 - Output BW: up to 6 GHz (3rd Nyq. Window @ 4 GHz CLK);
 - Connector: SSMC;
- **CLOCK INPUT CHARACTERISTICS:**
 - Input Mode: Single-ended;
 - Connector: SSMC;
 - Direct, PLL+VCO Distribution Mode or TCXO;
 - Input Clock Frequency: (10 – 350) MHz ;
- **DAC CHARACTERISTICS:**
 - DAC Resolution: 12 bit;
 - Analog Output format selectable: NRZ and RTZ;
 - Sampling Clock: up to 4 GHz.
- **Electrical Performance:**
 - SFDR: about 50 dBc
- **Internal or External Trigger Signal;**
- **FMC Format (VITA57.1) Conduction Cooled;**
- **ITAR FREE;**
- **Operative Temperature Range: (-40 , +85)°C.**



HS DAC FMC Board: Description

D.&P. has designed a solution to synthesize RF signal using the most innovative D/A converter by Euvis.

The HS DAC FMC is a FMC daughter card with an only output channel.

The output signal has a RF bandwidth up to about 6 GHz with a sampling clock of 4 GHz and generation performance has a good match with measurements described in the DAC datasheet, p.e. SFDR of about 50 dBc in RF BW.

The sampling clock can be direct and generated through a PLL+VCO using an external reference clock or a TCXO.

The HS DAC FMC Board is mechanically and electrically compliant to FMC standard (ANSI/VITA 57.1).

The board can be used in a conduction cooled environment.

The analog signals are AC coupled and in the front panel there are a few SSMC connectors for external connection.

